



## Transfer Functions of Switching Converters

Fast Analytical Techniques at Work with Small-Signal Analysis

Hi everyone, my name is Christophe Basso and I am releasing a new book on small-signal modeling (now available with [Faraday Press](#)). I have derived the control-to-output transfer functions of many converters, starting from simple dc-dc cells (buck, buck-boost or boost) to isolated versions like flyback or forward converters operated in voltage mode (VM), current mode (CM), quasi-resonance (QR), constant on-time (COT), constant off-time (FOT) with various operating modes like continuous conduction (CCM) or discontinuous conduction (DCM) and borderline conduction (BCM or CrM) for power-factor-corrected converters for instance. Many hours of calculus mainly using Mathcad® version 15.

I am releasing a set of 41 files which cover all the examples given in the book. Several files are of particular interest as they automate the compensation of the flyback converter operated in QR and fixed-frequency current-mode control: you select a crossover frequency and poles-zeroes are suggested with responses at high and low-line conditions.

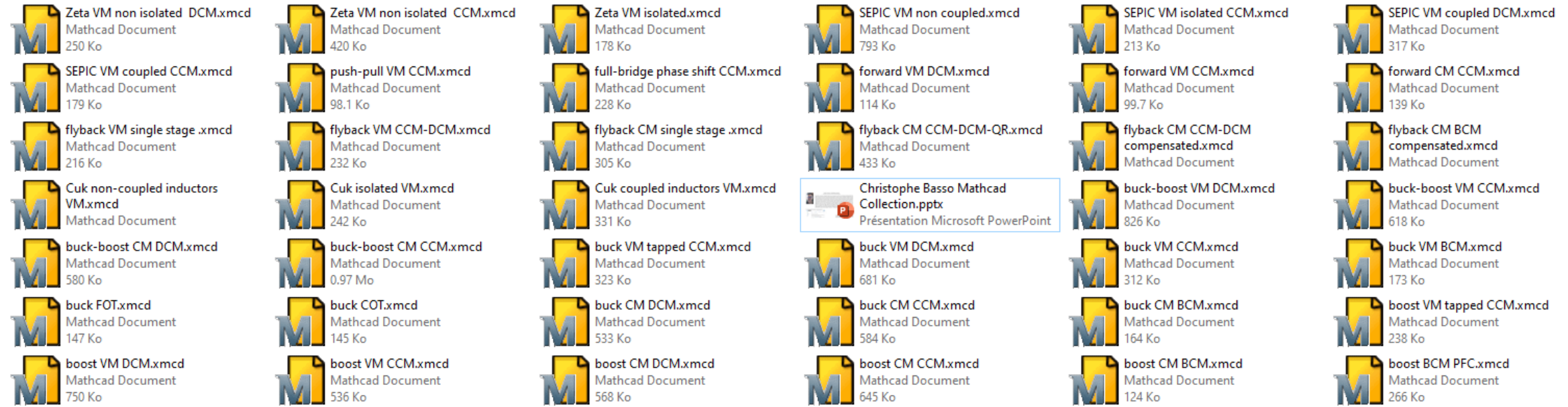
Considering the amount of work put in this study, I am distributing these files for a fixed fee that I will communicate by email: [cbasso@wanadoo.fr](mailto:cbasso@wanadoo.fr)

If you are a power supply designer, you can't miss these ready-made templates for your engineering job.

Thank you – Christophe Basso, May 2020.



## The Files – there are 41 sheets:



## Covered structures:

Buck: CM, VM, DCM/CCM, FOT, COT, QR

Buck-derived: forward, tapped, push-pull, phase-shift full bridge

Boost: CM, VM, DCM/CCM, QR, PFC

Buck-boost: CM, VM, DCM/CCM

Buck-boost-derived: flyback, single-stage PFC

SEPIC, Zeta, Cuk and LLC

# The File – select the operating parameters and plot the transfer functions

+ Buck-Boost Converter Fixed Switching Frequency and Voltage Mode - DCM

Christophe Basso - April 2021

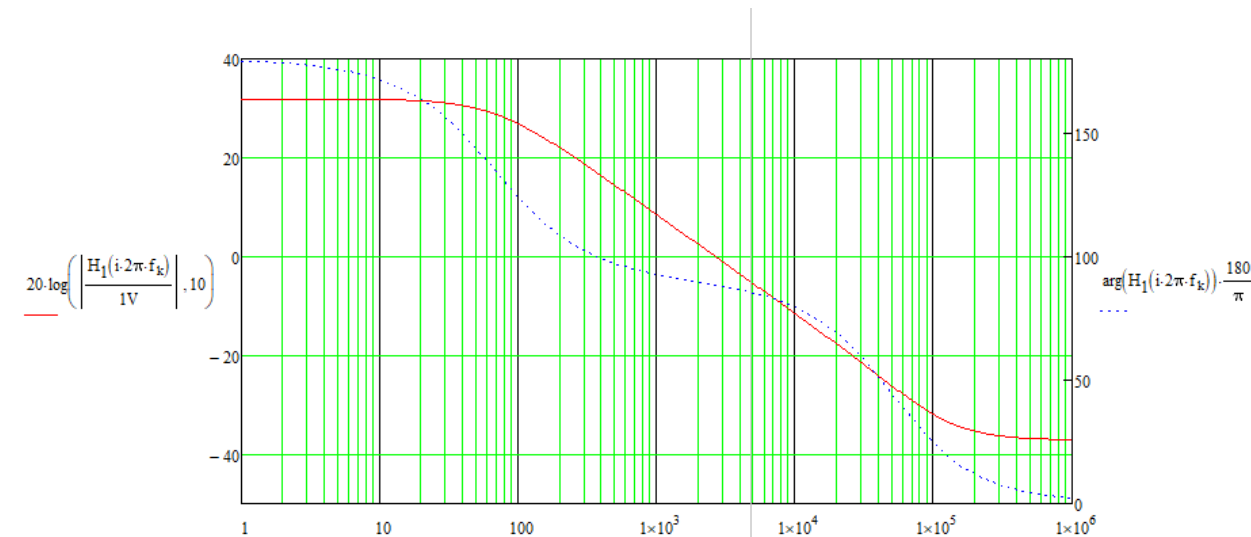
Transfer Functions of Switching Converters - Faraday Press

## Component values for the DCM boost converter

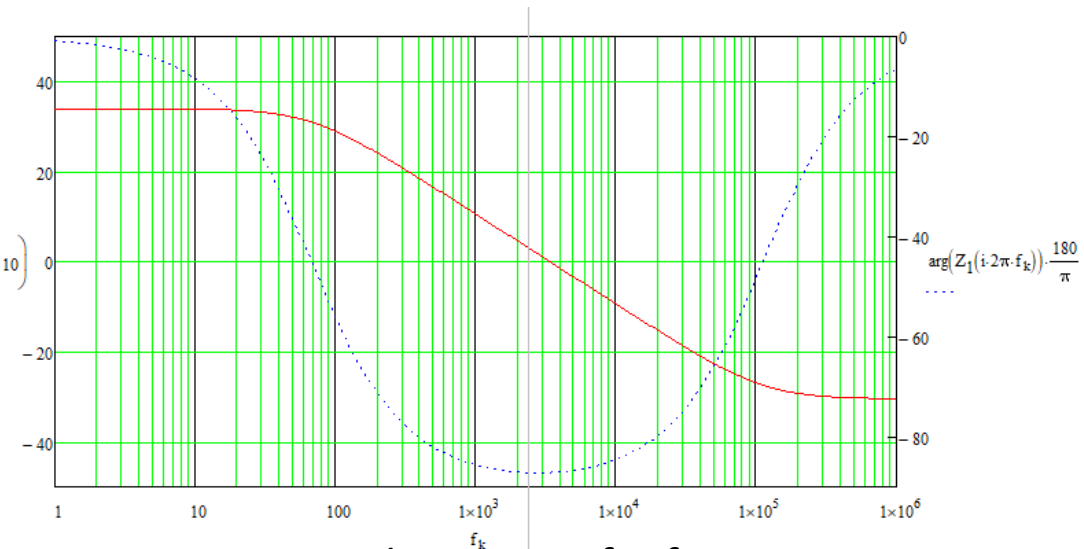
$$L_1 := 47\mu\text{H} \quad F_{\text{sw}} := 100\text{kHz} \quad C_2 := 47\mu\text{F} \quad V_{\text{in}} := 12\text{V} \quad r_L := 0.01\Omega$$

$$\tau_L := \frac{L_1}{R_L} \cdot F_{\text{sw}} \quad d_1 := 0.445 \quad R_L := 100\Omega \quad r_C := 0.03\Omega$$

$$\frac{2 \cdot F_{\text{sw}} \cdot L_1}{(1 - d_1)^2} = 30.517\Omega \quad \text{critical load resistance value}$$



Control-to-output transfer function



Output impedance transfer function

# Compensated sheets – select a crossover frequency and automate the poles and zeroes placement

## Converter Parameters Fixed-Frequency Operation

$$\begin{aligned}
 V_{in\_min} &:= 90V & V_{in\_max} &:= 370V & N_s &:= 16 & R_{sense} &:= 0.33\Omega & C_{out} &:= 680\mu F \\
 V_{out} &:= 19V & L_p &:= 600\mu H & N_p &:= 100 & \eta &:= 85\% & R_{esr} &:= 0.05\Omega \\
 P_{out} &:= 70W & V_f &:= 0.5V & N_1 &:= \frac{N_s}{N_p} = 0.16 & R_{load} &:= \frac{V_{out}^2}{P_{out}} = 5.157\Omega
 \end{aligned}$$

## Controller Parameters

$$F_{sw} := 65kHz \quad S_e := 0 \frac{kV}{s} \quad Div := 3 \quad T_{sw} := \frac{1}{F_{sw}} = 15.385\mu s$$

Operating points calculations - Vin\_min

Operating points calculations - Vin\_max

Logarithmic graph

Mode detection CCM or DCM for Vin\_min:

Mode detection CCM or DCM for Vin\_max:

Small signal Analysis Poles Zeros Positions - CCM operation - Vin\_min

Small signal Analysis Poles Zeros Positions - DCM operation - Vin\_min

Small signal Analysis Poles Zeros Positions - CCM operation - Vin\_max

Small signal Analysis Poles Zeros Positions - DCM operation - Vin\_max

Complete transfer function CCM and DCM - Vin min and max

Final result check LL

$$f_{cLL} := 1kHz$$

$$f_{olLL} := \text{root}(|T_{OL\_LL}(i \cdot 2\pi \cdot f_{cLL}, S_e)| - 1, f_{cLL})$$

$$f_{cLL} = 0.998\text{-kHz} \quad \text{crossover frequency}$$

$$\arg(T_{OL\_LL}(i \cdot 2\pi \cdot f_{cLL}, S_e)) = 68.761^\circ \quad \text{Phase margin}$$

$$f_{gLL} := 20kHz$$

$$f_{gLL} := \text{root}(\arg(T_{OL\_LL}(i \cdot 2\pi \cdot f_{gLL}, S_e)), f_{gLL})$$

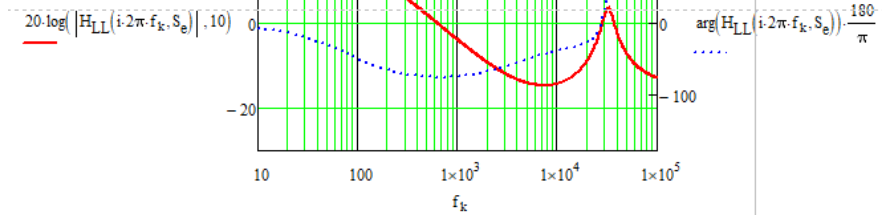
$$f_{gLL} = 23.953\text{-kHz} \quad f(p)$$

$$20 \cdot \log(|T_{OL\_LL}(i \cdot 2\pi \cdot f_{gLL}, S_e)|, 10) = -23.953 \quad \text{Gain margin}$$

## Power Stage at low line

Mode<sub>LL</sub> = "CCM"

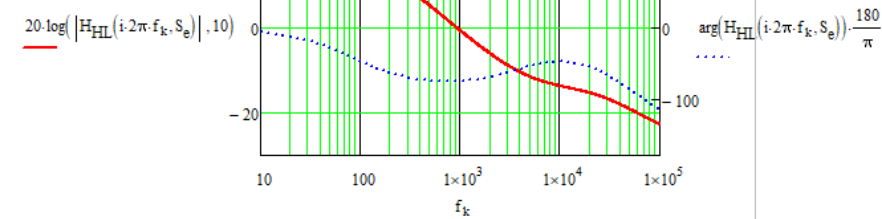
Duty<sub>LL</sub> = 0.575



## Power Stage at high line

Mode<sub>HL</sub> = "DCM"

Duty<sub>HL</sub> = 0.205



## Compensated Converter Bode - Low line

$$T_{OL\_LL}(s, S_e) := -(H_{LL}(s, S_e) \cdot G_1(s))$$

